NEC's CyberWorkBench and DeFacTo's SIGNOFF Interoperate Towards A Complete C to RTL Design Solution Including DFT

Grenoble, France, September 10th. NEC and DeFacTo Technologies S.A. today announced that DeFacTo Technologies' SIGNOFF has been integrated into NEC's CyberWorkBench C-based VLSI design suite.

Tightly integrating CyberWorkBench and SIGNOFF provides unique capabilities in the market. Among many, it allows users to explore different architectural scenarios from C to RTL including DFT criteria.

<u>CyberWorkBench</u> is a C-based electronic circuit design platform developed by NEC over the course of twenty years. CyberWorkBench is developed around the "All-in-C" paradigm that allows high level synthesis and verification of any ANSI-C or SystemC program generating high quality circuits.

SIGNOFF is an RTL DFT evaluation and enhancement platform. It helps reaching high DFT quality levels in a more predictable timeframe and allows earlier DFT analysis & enhancement at the block, the IP core and the top levels and makes possible RTL DFT exploration including test compression, memory/logic BIST & JTAG.

"Integrating SIGNOFF into CyberWorkBench will provide new capabilities to our users by including DFT analysis at the earliest design stages, enabling them to generate 'DFT friendly' RTL" said Kazutoshi Wakabayashi, Senior Manager, Embedded Systems Solution Division, NEC. "We were extremely pleased to learn how easy it is to use SIGNOFF and how smooth and easy the integration with CyberWorkBench went".

"We are excited about our collaboration with NEC. SIGNOFF has proven capabilities to generate DFT friendly RTL designs. With this collaboration with NEC for the first time in the market, a user will have full confidence that a generated RTL is a 100% DFT friendly", said Chouki Aktouf CEO of DeFacTo Technologies. Mr. Aktouf added, "CyberWorkBench users will rapidly see lower NRE due to DFT problems when generating RTL designs."

About NEC Corporation

NEC Corporation is a leader in the integration of IT and network technologies that benefit businesses and people around the world. By providing a combination of products and solutions that cross utilize the company's experience and global resources, NEC's advanced technologies meet the complex and ever-changing needs of its customers. NEC brings more than 100 years of expertise in technological innovation to empower people, businesses and society. For more information, visit NEC at www.nec.com.

About DeFacTo Technologies

DeFacTo Technologies is a leading provider of Design-for-Test solutions at RTL. DeFacTo solutions enable designers to achieve "Design & DFT" closure at RTL by delivering a high quality suite of tools, which cover planning, analysis, insertion and debug needs. DeFacTo EDA tools SIGNOFF and STAR are silicon proven and are being used by major semiconductor companies to increase fault coverage predictability by strengthening Design and DFT quality at RTL and also by eliminating communication gap between DFT engineers and RTL designers. DeFacTo is headquartered at 167 rue de Mayoussard, 38430 Moirans, France. For more information, visit us at <u>www.defactotech.com</u>.